ACHARYA INSTITUTE OF TECHNOLOGY
Dept. of ECE
Bengaluru

DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC61	COURSE ID	C311
COURSE TITLE		DIGITAL COMMUNICATIONS					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E1.1		Apply the concept of sampling, quantization, encoding and reconstruction in base band transmission system.					
C3E1.2		Analyze the performance of Digital Modulation Schemes in terms of probability of Error, Spectral Efficiency and Power					
C3E1.3		Determine quantization error, error rate, SNR and the power spectra of discrete PAM signal to estimate the performance of various signalling formats.					
C3E1.4		Analyze the performance of optimum receivers by applying theory of estimation and infer the performance of various spread spectrum techniques.					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC62	COURSE ID	C312
COURSE TITLE		Arm Microcontroller & Embedded Systems					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E2.1		Describe the architecture of ARM processors, Memory types and principles of RTOS					
C3E2.2		Explain ARM cortex M3 instruction set					
C3E2.3		Write an ALP by making use of appropriate instructions.					
C3E2.4		Analyze the performance of scheduling algorithms used in RTOS.					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC63	COURSE ID	C313
COURSE TITLE		VLSI Design					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E3.1		Analyze the characteristics and parameters of MOScircuits in VLSI design.					
C3E3.2		Discuss CMOS fabrication process, subsystems design and VLSI architectures.					
C3E3.3		Apply design rules to draw schematic and layout of MOS circuits .					
C3E3.4		Analyze the performance of CMOS circuits in terms of memory, speed, power and area.					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC64	COURSE ID	C314
COURSE TITLE		Computer Communication Networks					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E4.1		Describe network components, header/frame formats, & functionalities for data Communication					
C3E4.2		Apply message forwarding / routing schemes in TCP/IP model for end-to end data transmission.					
C3E4.3		Apply different medium access and control techniques for optimal resource utilization					
C3E4.4		Design Subnet masks and range of addresses for a given network.					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC651	COURSE ID	C315

COURSE TITLE		Cellular Mobile Communication					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E5.1		Expalin Cellular Concepts and mobile radio propagation with small scale and multipath fading					
C3E5.2		Estimate path loss, link budget, capacity and other cellular parameters					
C3E5.3		Comprehend the cellular architecture addressing and services					
C3E5.4		Analyze the GSM and CDMA technology					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15EC653	COURSE ID	C315
COURSE TITLE		Artificail Neural Networks					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E5.1		Describe various terminologies associated with Neural Networks in supervised/unsupervised learning.					
C3E5.2		Identify different neural network architectures, their limitations and appropriate learning rules for each of the architectures.					
C3E5.3		Select appropriate Neural Network Algorithm for any given Classification problem.					
C3E5.4		Analyze the performance of Neural networks.					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15ECL661	COURSE ID	C316
COURSE TITLE		Data Structures using C++					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E6.1		Understood the fundamental concepts of arrays, pointers, and linked lists using C++.					
C3E6.2		Able to apply arrays and linked list concepts to design and analyze stacks, queues and applications of these data structures to real time applications					
C3E6.3		Able to apply arrays and linked list concepts to design and analyze the skip lists, binary trees and applications of these data structures to real time applications					
C3E6.4		Able to apply arrays and linked list concepts to design and analyze the priority queues, binary search trees and applications of these data structures to real time applications					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15ECL663	COURSE ID	C316
COURSE TITLE		Digital System Design using Verilog					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E6.1		Apply the Systematic process for designing digital circuits using Verilog HDL.					
C3E6.2		Analyze and model different types of Memories used in Embedded Systems					
C3E6.3		Identify the different types of I/O controllers and requirements for interfacing I/O devices .					
C3E6.4		Design sequential circuits, data paths and develop a Verilog HDL model					
DEPARTMENT	ECE	SEMESTER	6	COURSE CODE	15ECL67	COURSE ID	C317
COURSE TITLE		Embedded Controller LAB					
COURSE OUTCOME NO		COURSE OUTCOME STATEMENTS					
C3E7.1		Identify the ARM Cortex M3 Microcontroller Instruction Set and Hardware devices					
C3E7.2		Demonstrate the Instruction set of ARM cortex M3 Microcontroller in					

		Programming					
C3E7.3		Use the External hardware to Interface Peripheral devices with LPC1768 Microcontroller.					
DEPARTMENT	**ECE**	**SEMESTER**	**6**	**COURSE CODE**	**15ECL68**	**COURSE ID**	**C318**
COURSE TITLE		**COMPUTER NETWORKS LAB**					
COURSE OUTCOME NO		**COURSE OUTCOME STATEMENTS**					
C3E8.1		Implement & Analyze Transport & Network layer protocols using Network Simulator tool.					
C3E8.2		Simulate Wired & wireless network environment to compute the network parameters.					
C3E8.3		Write C/C++ Programs to implement routing algorithms in different Network topologies.					
C3E8.4		Develop C/C++ Programs for framing & transmission of data.					